

Figure 1

1	2	3	4	5	6	7	8	9	10
✓	✓	X	X	✓	✓	✓	X	X	X
		▲	▲				▲	▲	▲

✓ = Packets received

X = Packets NOT received

Figure 2A
Application Send Thread

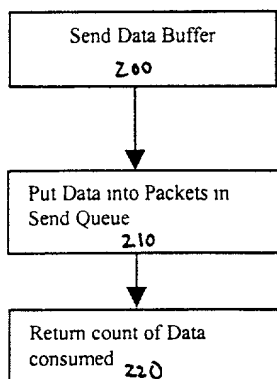


Figure 2B
ATP Send Thread

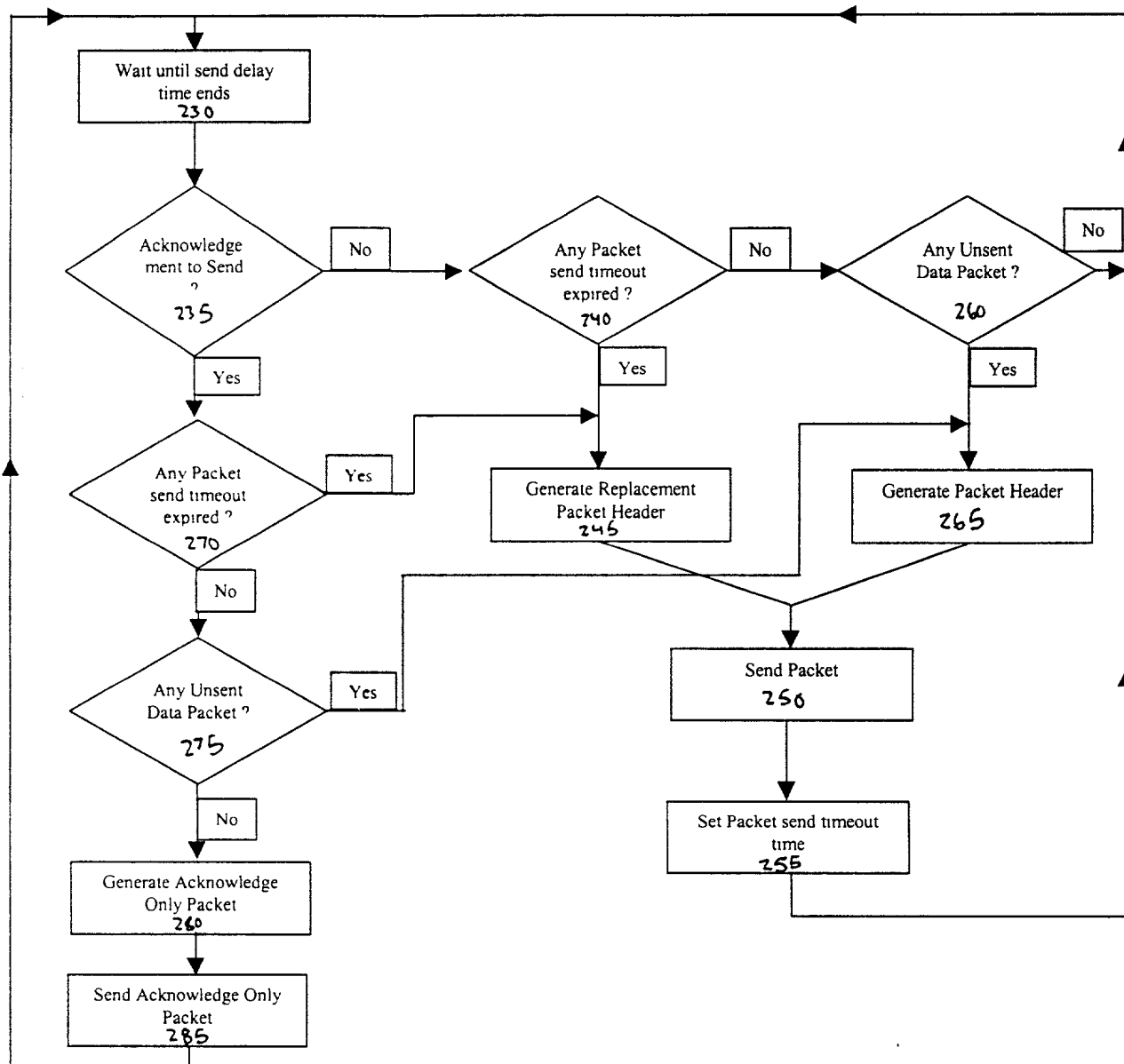
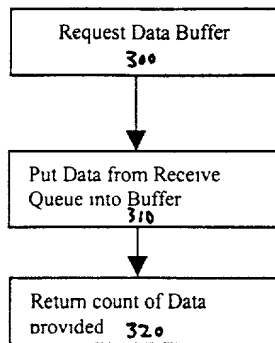
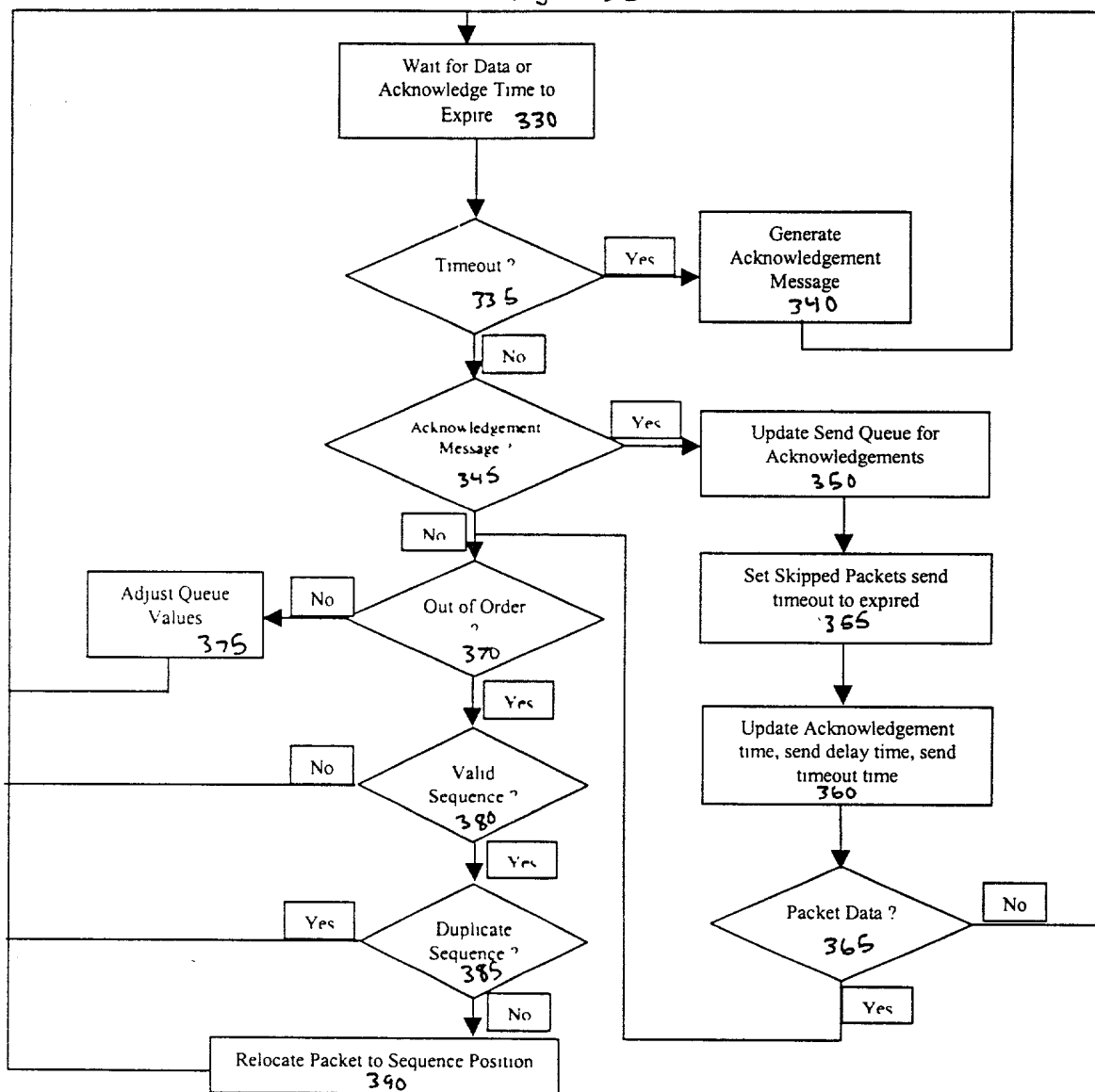


Figure 3A
Application Receive Thread



ATP Receive Thread
Figure 3B



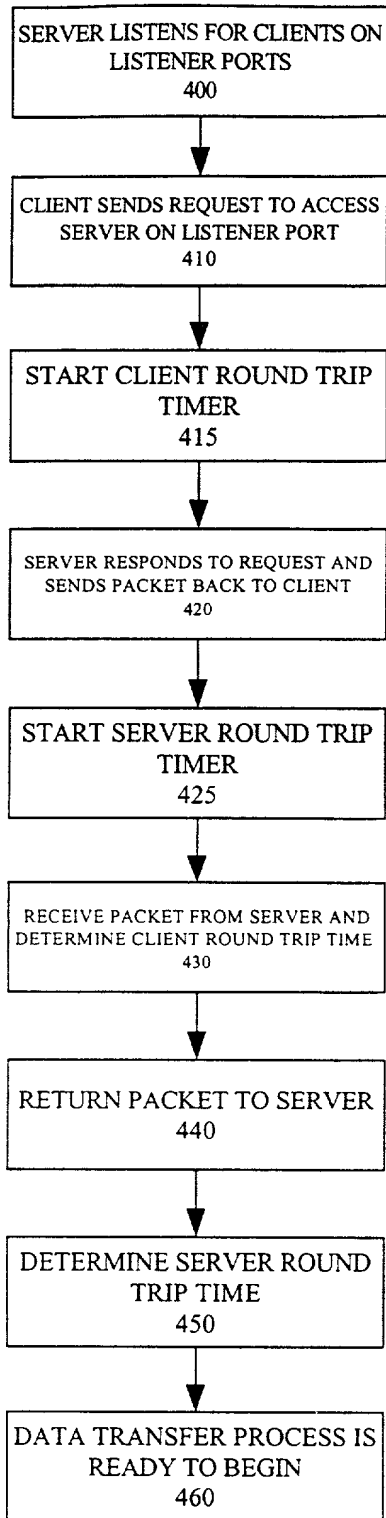


FIGURE 4

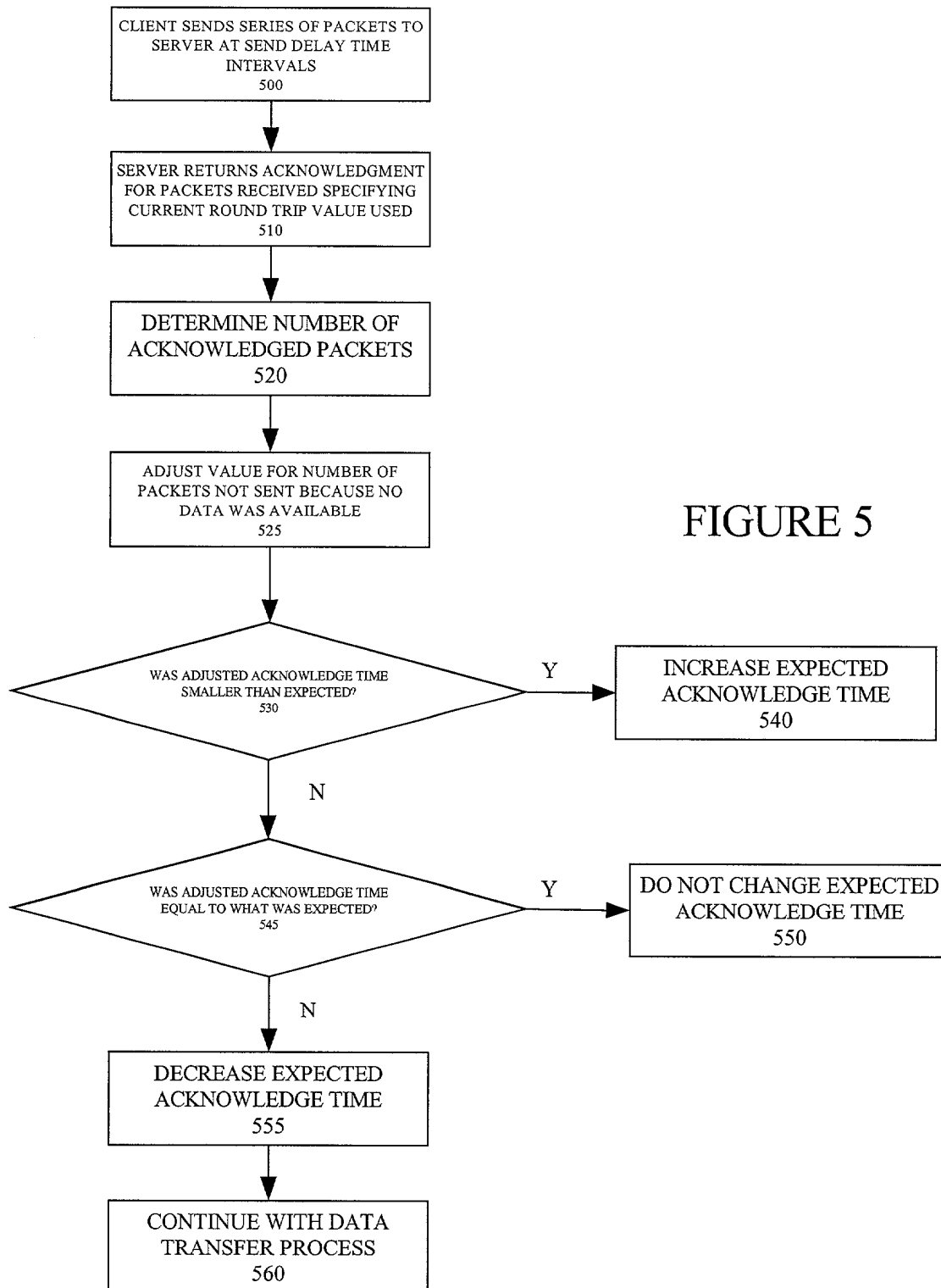
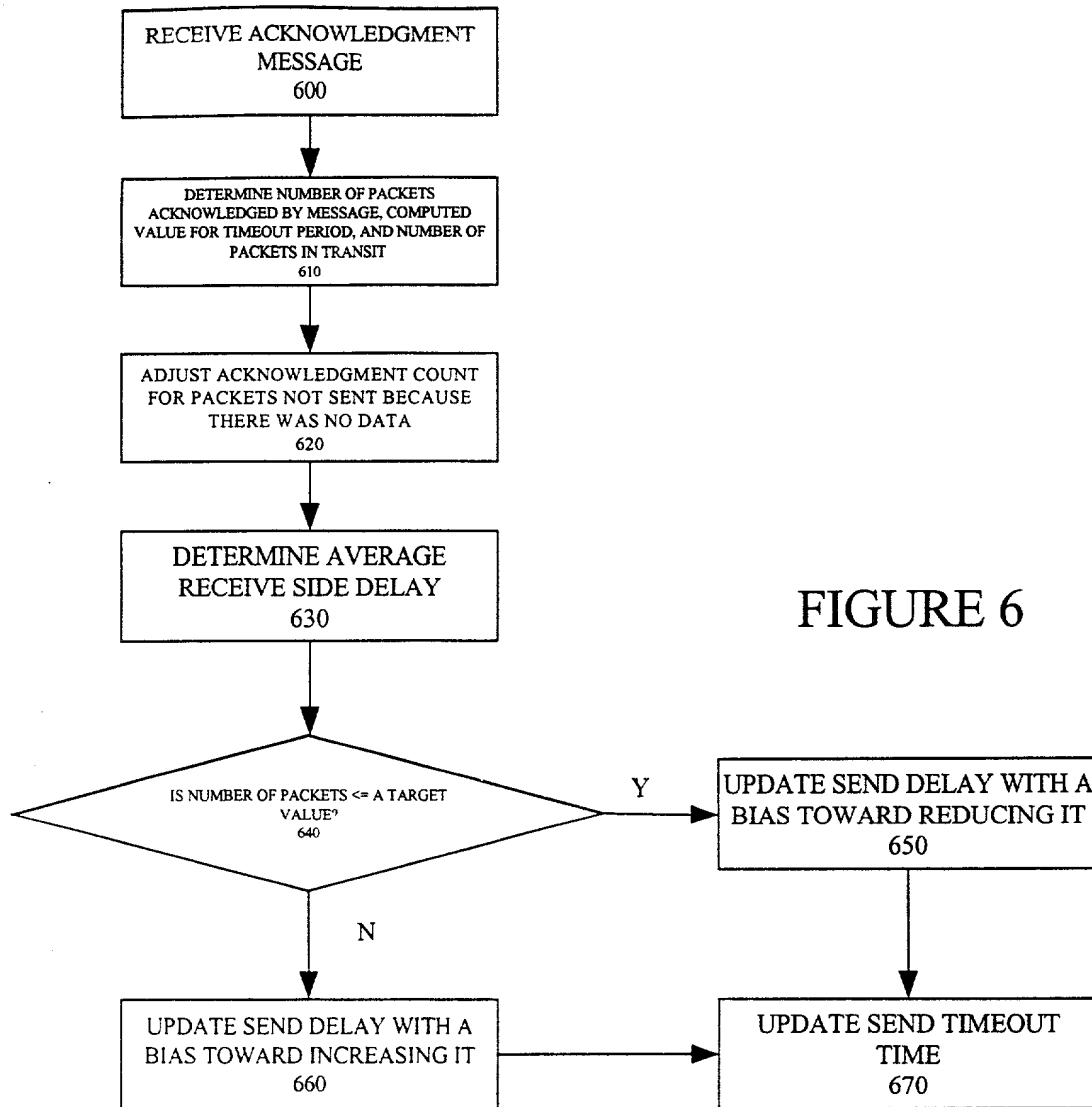


FIGURE 5



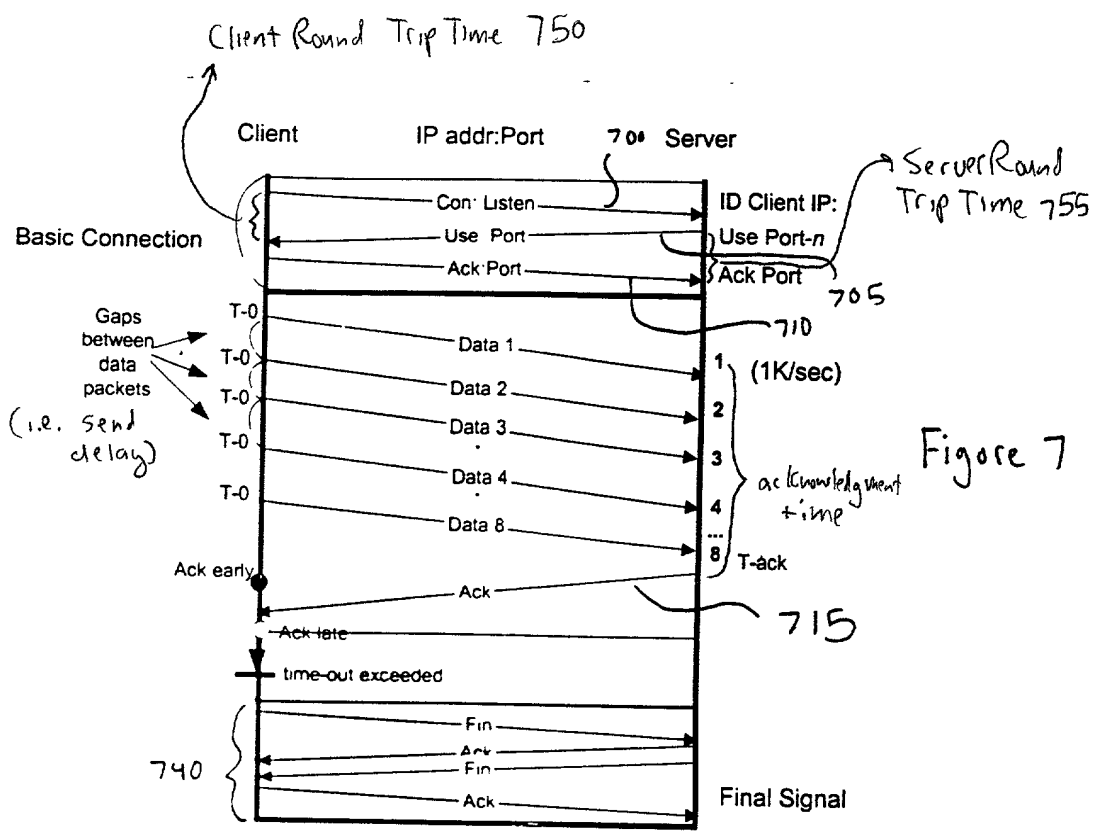


Figure 7

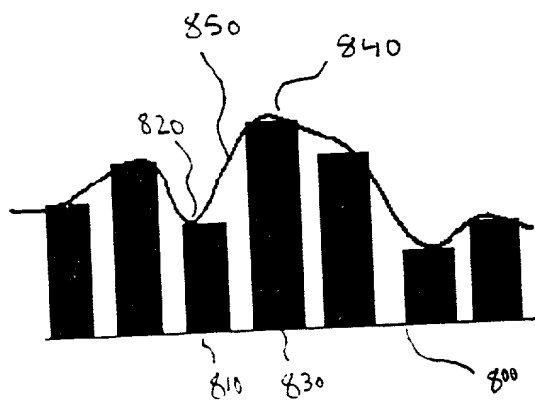


Figure 8


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graph TD
    A["(A) App Send Thread 900"] --> B["(B) Packetizer 910"]
    B --> C["(C) UDP Send Queues 920"]
    C --> D["(D) UDP Send Thread 930"]
    D --> E["(E) Send/Recv ACK"]
    E --> F["(F) UDP Recv Thread 950"]
    F --> G["(G) UDP Recv Queue 960"]
    G --> H["(H) Re-Sequencer 970"]
    H --> I["(I) App Recv Thread 980"]
    
    subgraph "Sending ACK & data"
        F
    end
    
    subgraph "Receiving ACK & data"
        I
    end
  
```

Figure 9 illustrates the sequence of operations for sending and receiving data. The diagram shows the flow of data and control signals between various components:

- (A) App Send Thread 900 sends data to (B) Packetizer 910.
- (B) Packetizer 910 sends data to (C) UDP Send Queues 920.
- (C) UDP Send Queues 920 send data to (D) UDP Send Thread 930.
- (D) UDP Send Thread 930 sends data to (E) Send/Recv ACK.
- (E) Send/Recv ACK sends data to (F) UDP Recv Thread 950.
- (F) UDP Recv Thread 950 sends data to (G) UDP Recv Queue 960.
- (G) UDP Recv Queue 960 sends data to (H) Re-Sequencer 970.
- (H) Re-Sequencer 970 sends data to (I) App Recv Thread 980.

The diagram also shows the flow of data and control signals between the components, including the sending of ACK and data, and the receiving of ACK and data.

Figure 9

Figure 10

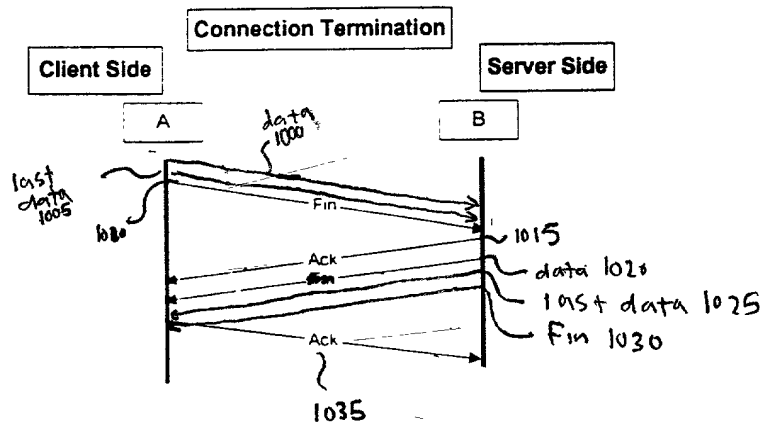
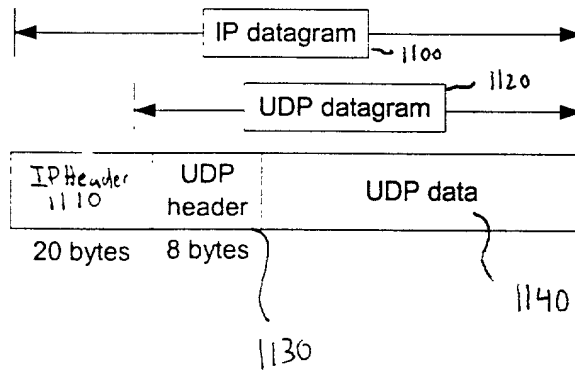


Figure 11



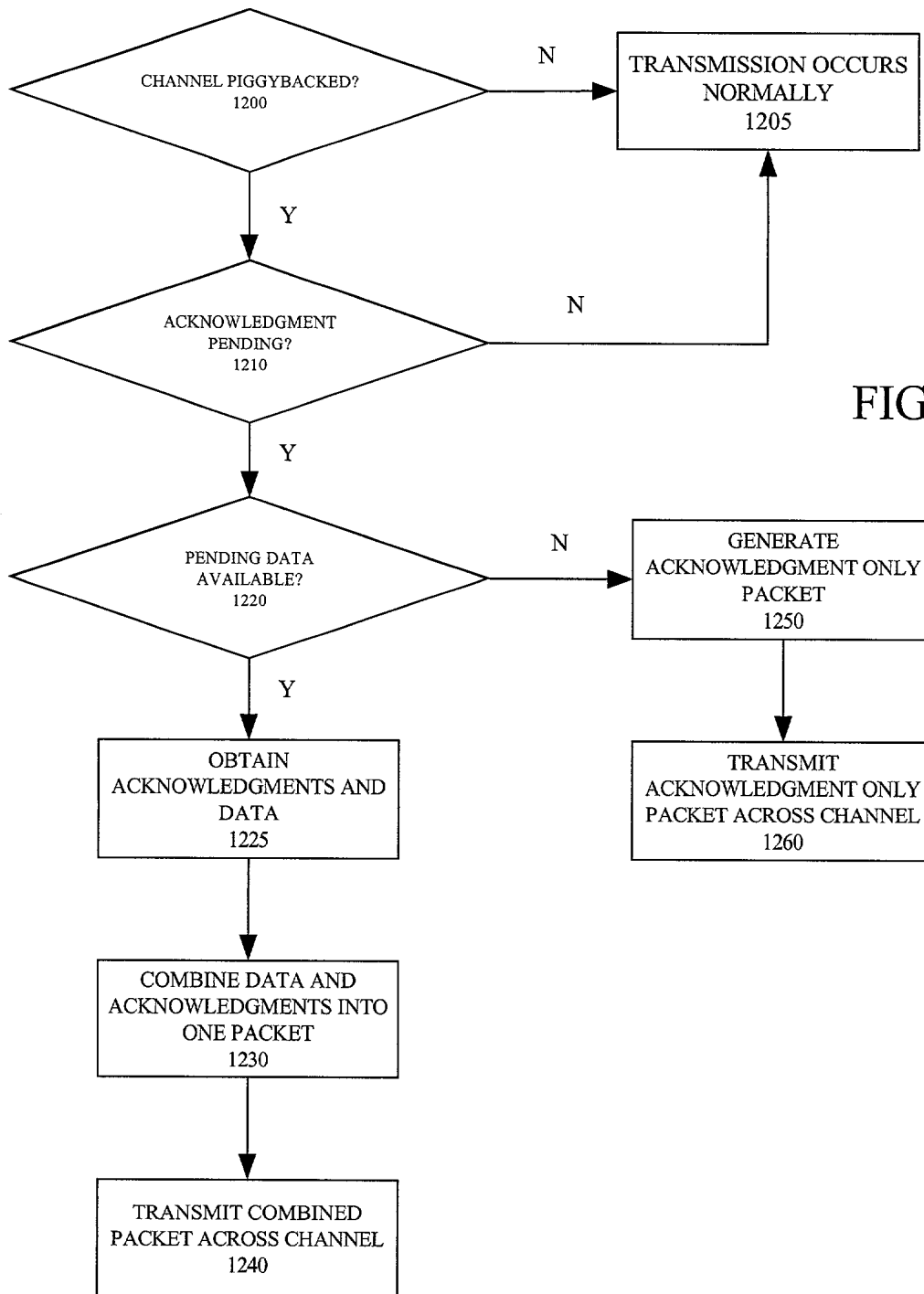


FIGURE 12

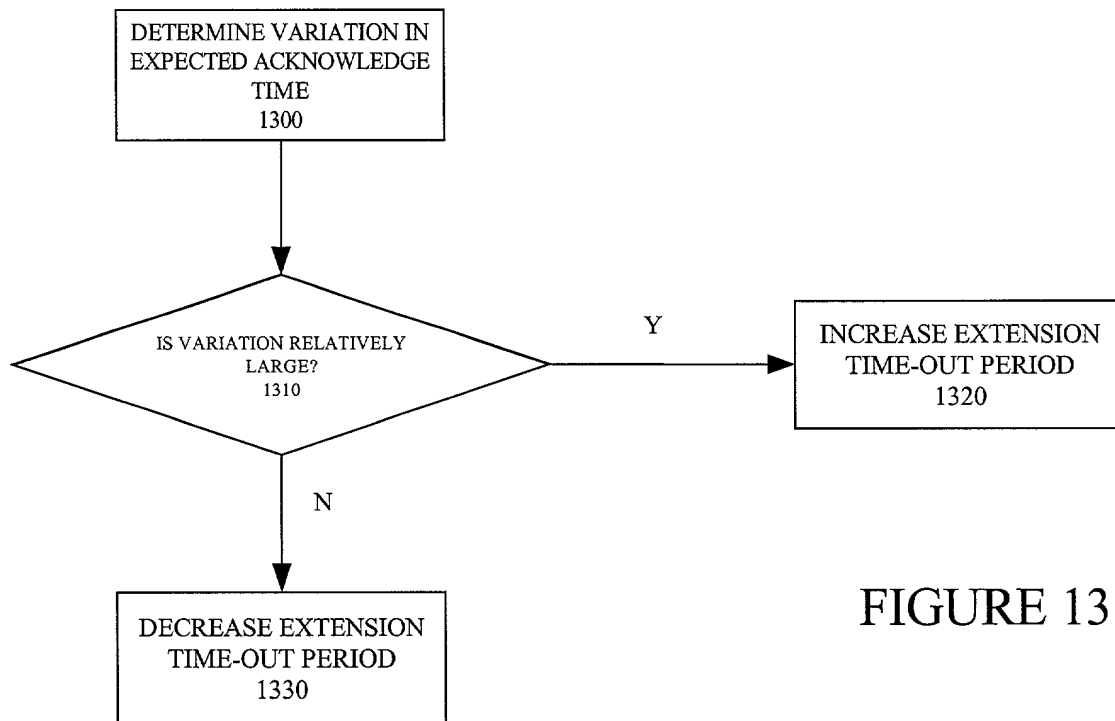


FIGURE 13

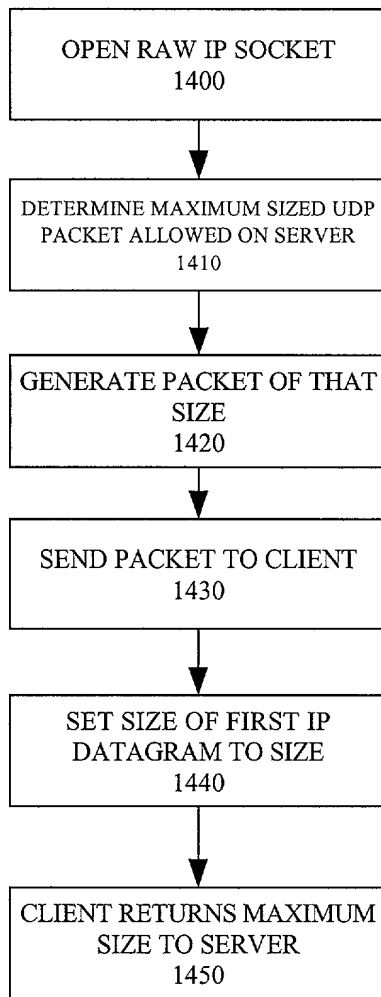


FIGURE 14

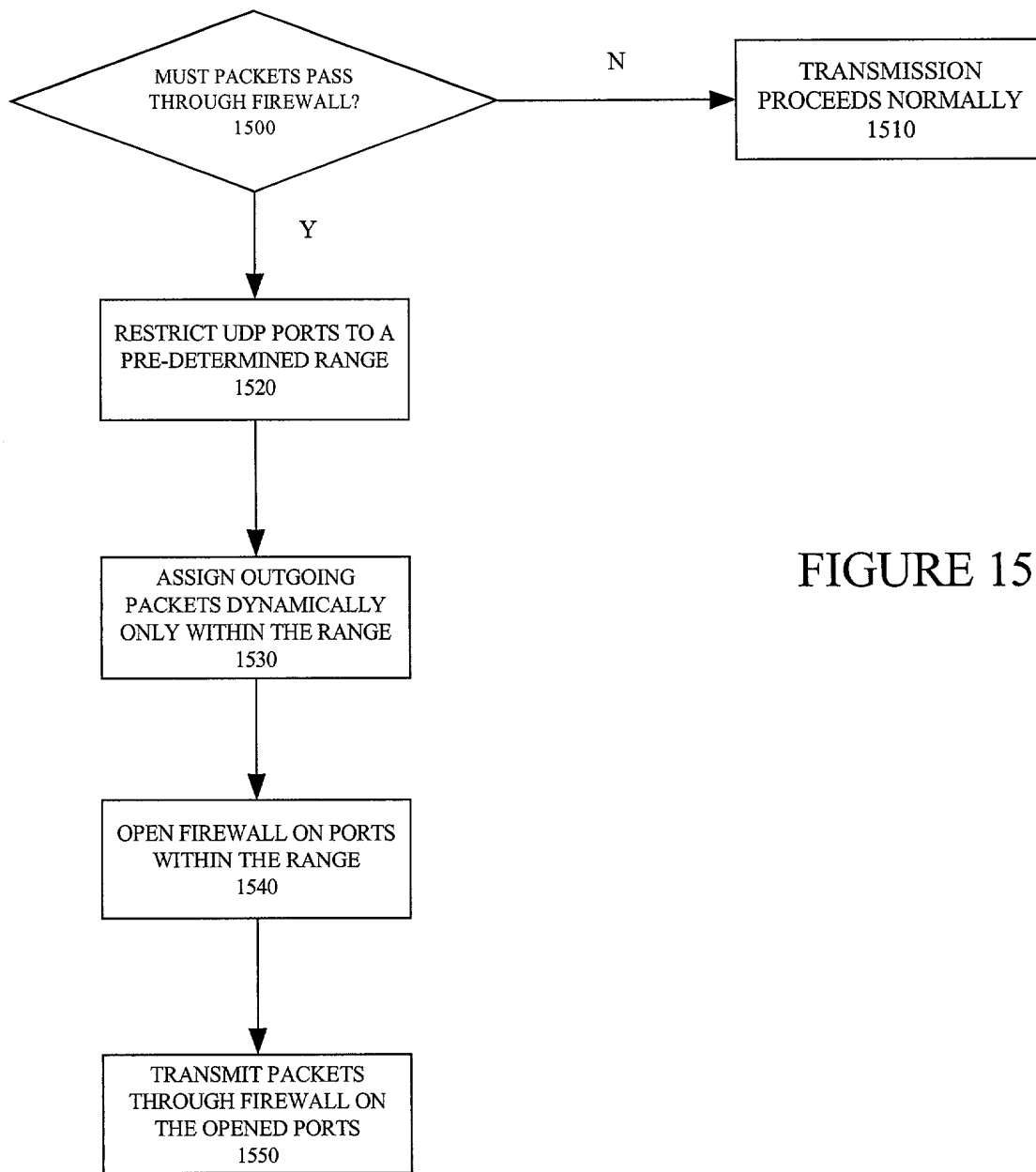


FIGURE 15

Version	IHL	TOS	Length	
Identification			Flags	Fragment Offset
Time To Live		Protocol	Checksum	
Source IP Address				
Destination IP Address				

Figure 16

Source Port			Destination Port		
Sequence Number					
Acknowledgement Number					
Offset	Reserve	Flags		Window	
Checksum			Urgent Pointer		
Options (if used)					Padding

Figure 17